

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(hardware with accelerat\$3) and point\$3 and ((high adj level adj language) (general adj purpose adj language) "C++" ("C" near3 language) pascal java "C/C++") and (part portion partition\$3 segment fragment\$5 section point\$3) and (hdl or vhdl or verilog) and (accelerat\$3 same (memory rom ram cache) and ((programmable adj device) pld pla fpga (programmable adj circuit))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 16:13
L2	0	(hardware with accelerat\$3) and ((point\$3 register) same port) and ((high adj level adj language) (general adj purpose adj language) "C++" ("C" near3 language) pascal java "C/C++") and (part portion partition\$3 segment fragment\$5 section point\$3) and (hdl or vhdl or verilog) and (accelerat\$3 same (memory rom ram cache) and ((programmable adj device) pld pla fpga (programmable adj circuit))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 16:14